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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re PATENT APPLICATION of:

Group Art Unit: 2822  
Examiner: Kiesha L. Rose  
Confirmation No.: 2321

Applicant(s): Motoki KOBAYASHI et al.

Serial No.: 10/673,499

Filing Date: September 30, 2003

For: SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR DEVICE  
FABRICATION METHOD

Atty. Dkt.: FUJI 137

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June 27, 2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is in response to the Notice mailed on June 21, 2006. Please charge our Deposit Account No. 18-0002 if any fees are needed to enter this paper, and consider this paper a petition for extension of time if needed.

Respectfully submitted,

*Nick Bromer*

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**No. 18-0002**

June 13, 2005

Date